

<b>Notice of References Cited</b>		Application/Control No. 10/073,506	Applicant(s)/Patent Under Reexamination FALSTER ET AL.	
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*	Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
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B	US-5935320	08-1999	Graef et al.	117/2
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*	Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages
U	Wolf et al., Silicon Processing for the VLSI Era, Vol 1.: Process Technology, Chapter 2, Lattice Press, Sunset Beach, CA, USA, pp. 36-72, 1986.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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*Matthew Anderson* 1/28/04

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